

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (currently amended) A system interface, comprising:
a packet switching network comprising a plurality of crossbar switches;
a cache memory;
a plurality of directors, one portion of such directors being ~~adapted for coupling~~ coupled to a host computer/server and another portion of the directors being adapted for coupling to a bank of disk drives, the plurality of directors and cache memory being interconnected through the packet switching network;
each one of the directors ~~being is~~ coupled to a corresponding one of the plurality of crossbar switch switches;
wherein the ~~cross bar~~ corresponding one of the plurality of crossbar switches switch is directly connected to at least two other ones of the ~~cross bar switches networks~~ plurality of crossbar switches and indirectly connected to of other ones of the crossbar switches through the at least two directly connected crossbar switches.

Cancel claim 2

3. (currently amended) The system interface recited in claim ~~2~~ 1 wherein the system interface includes a plurality of the packet switching networks, each one of the packet switching networks including:
a plurality of ~~cross bar~~ crossbar switches, ~~each cross bar~~ each crossbar switch being connected: (1) directly to crossbar switches of at least two other ones of crossbar switches in such one of the packet switching networks and to at least two one

~~of of the cross-bar crossbar~~ switches of a second one of the plurality of packet switching networks; and, (2) indirectly to other ones of the crossbar switches of such one of the packet switching networks via the crossbar switch directly connected to such one of the packet switching network, two of said at least two other ones of the crossbar switches in such one of the packet switching networks being connected indirectly to ~~two cross-bar~~ other ones of the crossbar switches of the second one of the packet switching networks, the two ~~cross-bar crossbar~~ switches of the second one of the packet switching networks being ~~cross-bar crossbar~~ switches connected indirectly to said one of the ~~cross-bar crossbar~~ switches of the second one of the plurality of packet switching networks.

Cancel claim 4

5. (currently amended) The system interface recited in claim 4 1 wherein the cache memory includes a plurality of cache memory sections, each one of such sections being coupled to a corresponding one of the crossbar switches.

6. The system interface recited in claim 5 wherein the packet switching network comprises the crossbar switches of the directly and indirectly connected crossbar switches.

7. (currently amended) The system interface recited in claim 6 wherein the system interface includes a plurality of the packet switching networks, each one of the packet switching networks including:

a plurality of ~~cross-bar crossbar~~ switches, each ~~cross-bar crossbar~~ switch being connected: (1) directly to crossbar switches of at least two other ones of crossbar switches in such one of the packet switching networks and to at least two ~~one of the cross-bar crossbar~~ switches of a second one of the plurality of packet switching networks; and, (2) indirectly to other ones of the crossbar switches of such one of the packet switching networks via the crossbar switch directly connected to such one of the packet switching network, two of said at

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least two other ones of the crossbar switches in such one of the packet switching networks being connected indirectly to other ones of the ~~two cross-bar~~ crossbar switches of the second one of the packet switching networks, the two ~~cross-bar~~ crossbar switches of the second one of the packet switching networks being ~~cross-bar~~ crossbar switches connected indirectly to said one of the ~~cross-bar~~ crossbar switches of the second one of the plurality of packet switching networks.